

FIG. 1 is a block diagram of a system architecture. The system includes a Motherboard Section (10), a System Connection Back Plane (20), a System Management Unit (30), and Peripheral Devices (40). A Switch Control Signal (50) is sent from the System Management Unit (30) to the System Connection Back Plane (20).

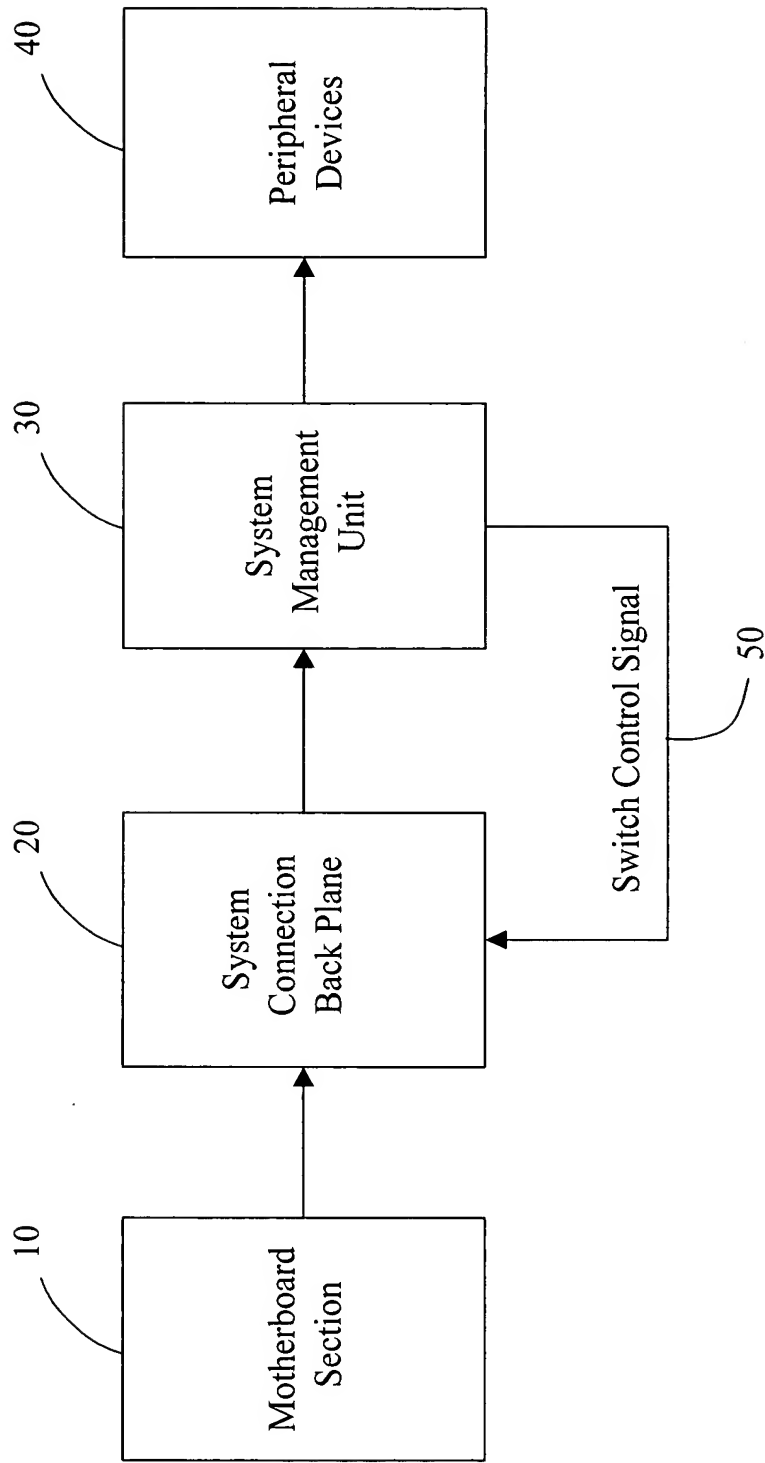


FIG.1

FIG. 2 is a block diagram of a system 10. The system 10 includes a Motherboard USB Port 60, a Switching Module 70, a USB Extension Device 80, and a Peripheral Device 40. The Motherboard USB Port 60 is connected to the Switching Module 70. The Switching Module 70 is connected to the USB Extension Device 80. The USB Extension Device 80 is connected to the Peripheral Device 40. A Switch Control Unit 90 is connected to the Switching Module 70 and the USB Extension Device 80.

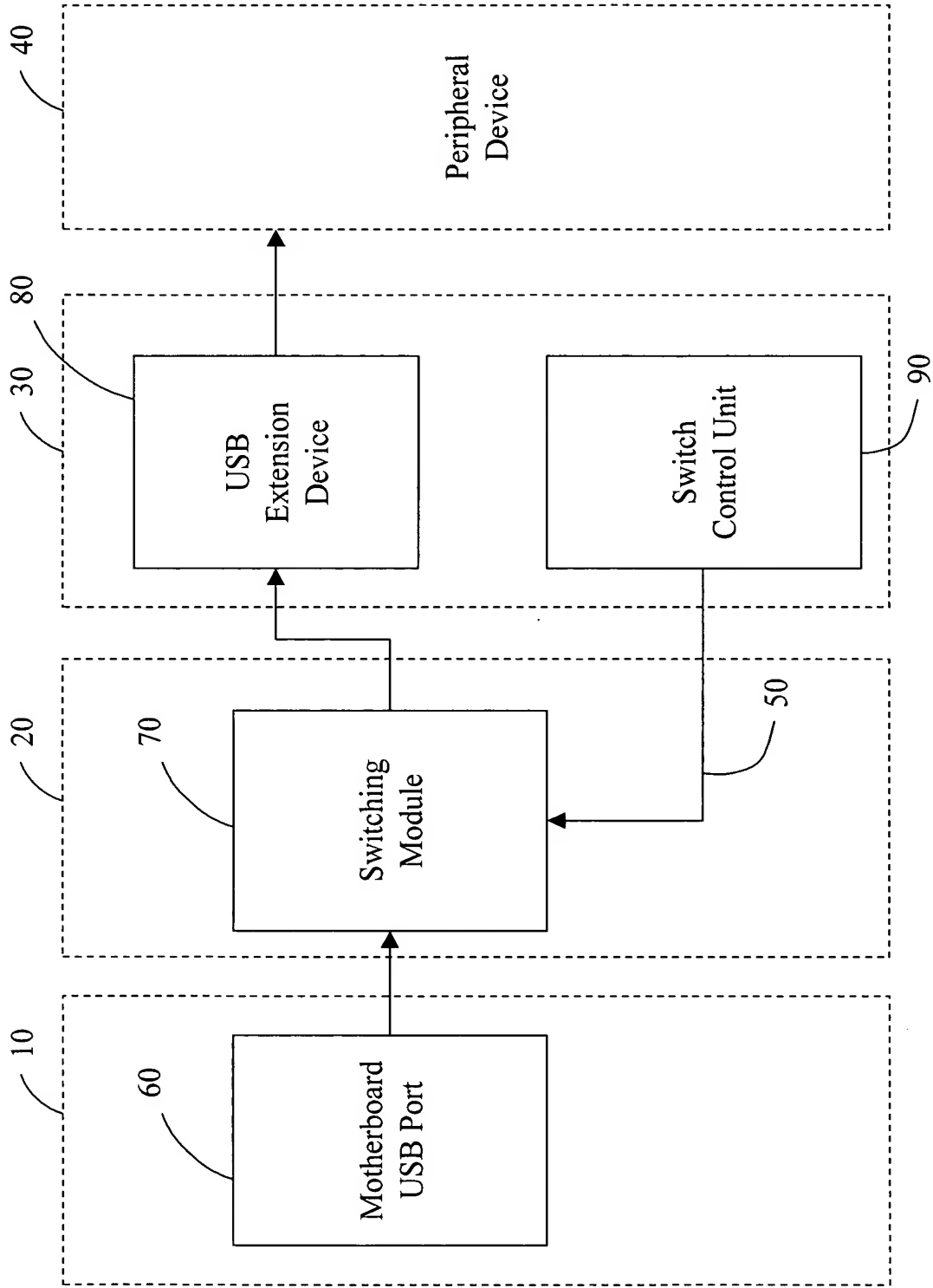


FIG.2

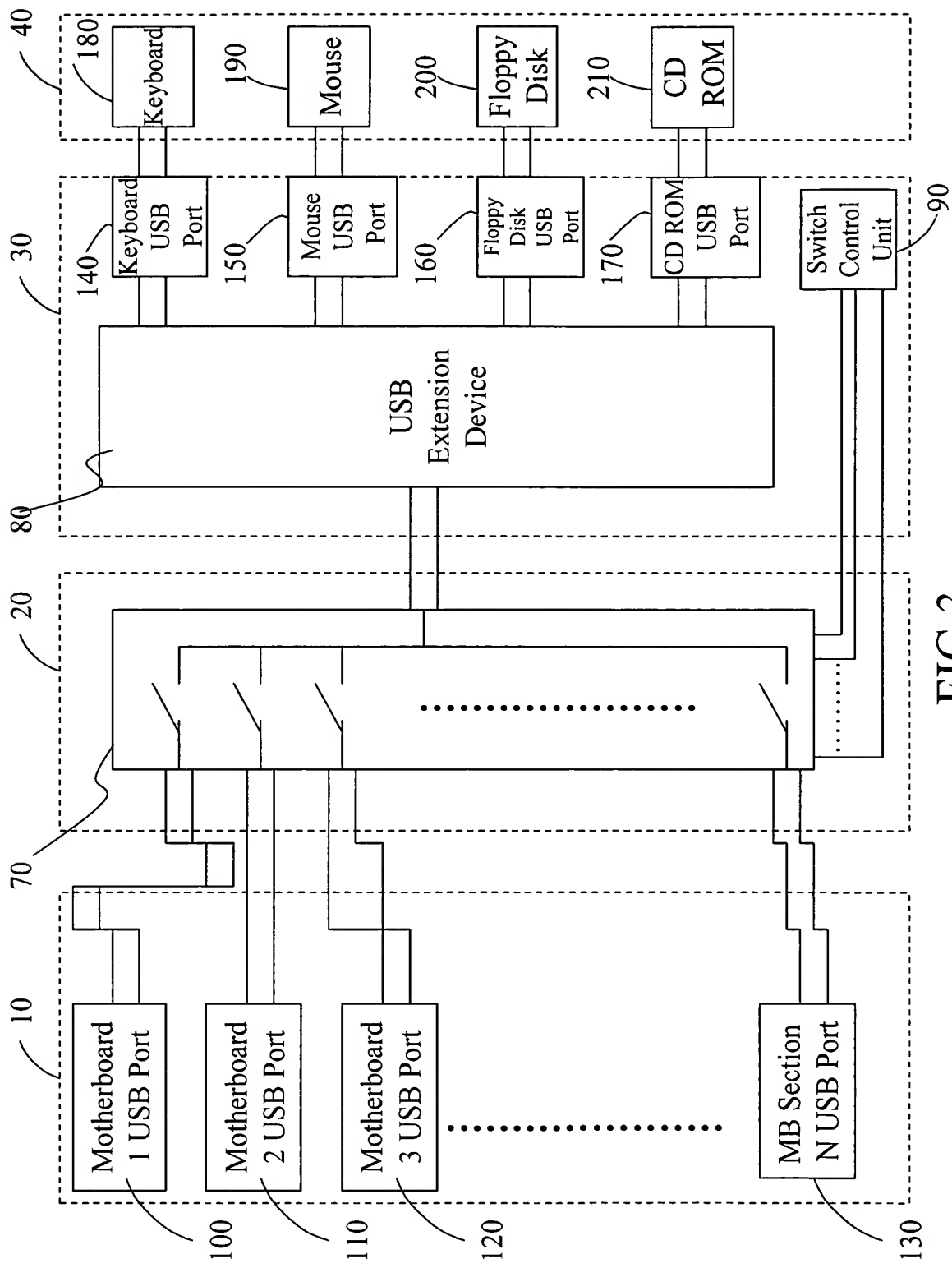


FIG.3